

10/584617

IAP11 Rec'd PCT/PTO 26 JUN 2006

Docket No.: 071971-0643

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shunsaku MURAOKA, et al. :

Application No.: Not Yet Assigned : Group Art Unit: Not Yet Assigned

Filed: June 26, 2006 : Examiner: Not Yet Assigned

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For: MEMORY DEVICE, MEMORY CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT HAVING VARIABLE RESISTANCE

PRELIMINARY AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

Amendments to the Specification are on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments are on page 15 of this paper.